

Electronic Acknowledgement Receipt

EFS ID:	1538008
Application Number:	10618113
International Application Number:	
Confirmation Number:	8697
Title of Invention:	Wafer-level chip scale package and method for fabricating and using the same
First Named Inventor/Applicant Name:	Rajeev Joshi
Customer Number:	27966
Filer:	kenneth E. Horton/Hailey Hunsaker
Filer Authorized By:	kenneth E. Horton
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)

1	Amendment After Final	USPTO_RCE_11948_21.pdf	369507	no	7
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	RCE3.pdf	243355	no	3
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8198	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				621060	
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